

In the Abstract:

Please substitute the following Abstract for the Abstract currently of record:

A thin film transistor matrix device including an insulating substrate, a plurality of thin film transistors arranged on the insulating substrate, and a plurality of picture element electrodes arranged on the insulating substrate in a matrix and connected to the thin film transistors. The device also includes a plurality of bus lines made of a first conducting film, a first insulating film formed on the first conducting film, a second conducting film formed on the first insulating film, and a second insulating film formed on the first insulating film and the second conducting film. Additionally, first and second contact holes are formed outside an image display region in which the plurality of picture elements are formed. The first conducting film is connected to the third conducting film via the first contact hole, and the second conducting film is connected to the third conducting film via the second contact hole.